

# Agilent Technologies 81133A and 81134A, 3.35 GHz Pulse/Pattern Generators

Data Sheet- Revision 1.13



## Key features

- Pulse, data pattern and PRBS generation from 15 MHz up to 3.35 GHz
- Data formats NRZ, RZ and R1
- Low jitter, high accuracy
- PRBS generation from  $2^5 - 1$  to  $2^{31} - 1$
- Jitter emulation up to  $\pm 250$  ps
- Easy-to-use graphical user interface
- 50 mV - 2 V<sub>pp</sub> output amplitude covering LVDS applications
- Differential Outputs
- 1 or 2 channels

## 81133A and 81134A 3,35 GHz Pulse/Pattern Generators

The need for pulse and pattern generation is fundamental to digital device characterization tasks. The ability to emulate the pulse and pattern conditions to which the device will be subjected is essential. This emulation should include both typical and worst case conditions. Accurate emulation requires superlative signal integrity and timing performance along with full control over parameters that allow specific worst case testing.

## Setting Standards

The Agilent 81133A and 81134A 3.35 GHz Pulse Generators provide programmable pulse periods from 15 MHz (66.6 ns) to 3.35 GHz (298.5 ps) pulse capability on all channels. At these frequency ranges the transition time performance becomes critical; less than 60 ps is specified and with a RMS jitter of 1.5 ps typical signal quality is assured. The Delay Control Input and the Variable Crossover Point functionalities allow emulation of real world signals by adding jitter to clock or data signals or by distorting the 'eye' for eye diagram measurements.



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## Connectors

### Front Panel Connectors

All signal outputs and inputs are accessible at the front panel. These are:

- 2 (or 4) Output Connectors for the 1 (or 2) differential channel(s)
- Trigger Output
- Clock Input
- Start Input
- 1 (or 2) Delay Control Input(s) for the 1 (or 2) channel(s)

### Rear Panel Connectors

Remote programming interfaces: GPIB, LAN, USB (see also 'Additional Features')

## Clock Source

Selecting the clock source determines the origin of the time base. All other timing parameters are derived from it. There are two choices:

### Internal

The Clock is derived from the internal YIG oscillator.

### External

The Clock is derived from the external input. The frequency is measured and used to calculate frequency dependant values including the pulse width or phase if set to duty cycle mode or phase mode respectively.

### External 10MHz Reference

A 10 MHz reference clock can be applied to the clock input. This clock is used as a reference for all timing parameters.

### Direct Mode (Direct Internal/Direct External)

The direct modes allow changes of frequency without dropouts in the range of 1:2. This can be used for applications, where dropouts would make a measurement impossible (e.g.: PLL frequency sweep, micro processor clock sweep). In both direct modes, the delay and deskew of all channels is set to zero (deskewed at the connectors) and can't be changed. Square mode, data mode (NRZ only) and PRBS mode (NRZ only) are available. In 'Direct External' mode the PLL is bypassed and the instrument follows the external frequency exactly.

## Frequency/Period

The main frequency is set for all channels. The frequency can be divided further for each channel individually. The frequency can also be set as period.

## Main Modes

### Pulse/Pattern Mode

In pulse/pattern mode, each channel can be set independently to one of the channel modes described in 'Channel Modes'.

### Burst Mode

Burst mode enables the output of a burst consisting of data repeated N times followed by continuous zero data. It can be started either by:

- applying a signal at the start input.
- the start button.
- sending a command through the remote connections.

### Repetitive Burst Mode

This command selects a repeated burst consisting of data repeated N times followed by a pause of P times zeros of the same length as the data before the data is repeated.

## Channel Modes

The following channel modes are available, if the instrument main mode is set to pulse/pattern:

Note: The frequency of each channel can be optionally divided by 1, 2, 4, 8, 16, 32, 64, 128.

### Square

Generates a square wave (clock) of fixed width (50% duty cycle)..

### Pulse

Generates pulses with selectable width or duty cycle.

### Data

Generates data in either RZ, R1 or NRZ format. In RZ and R1 mode, the pulse width can be selected as either width or duty cycle.

### PRBS

Outputs a selectable PRBS (pseudo random binary sequence) polynomial of either RZ, R1 or NRZ format. In RZ and R1 mode, the pulse width can be selected as either width or duty cycle.

## Timing

### Delay

The delay can be set:

- As absolute value in nano seconds or pico seconds. The delay remains unchanged as the frequency or the period is modified.
- As phase (degrees relative to period). The phase remains unchanged as the frequency or the period is modified.

### Deskew

The deskew adjustment allows for the compensation of e.g. cable delays. Deskew adjustment is not available in Direct Mode. In this case, all channels are factory deskewed at the front panel connectors.

### Width

There are two ways to set the pulse width:

- As absolute value in nano seconds or pico seconds. In absolute mode, the pulse width stays constant when the frequency or period is changed.
- As duty cycle (percentage of period). In duty cycle mode, the duty cycle stays constant when the frequency or period is changed.

**NOTE:** Width adjustment is not available if data mode is set to NRZ.

## Pulse Format

### RZ

Return to zero pulse format. On 0 bit patterns, the signal remains at the low level. On 1 bit patterns, the signal goes high and back to the low level after the time specified by the pulse width or the duty cycle parameter.

### R1

Return to one pulse format. On 1 bit patterns, the signal remains at the high level. On 0 bit patterns, the signal goes low and back to the high level after the time specified by the pulse width or the duty cycle parameter.

### NRZ

Non-return to zero pulse format. The signal remains at the low level or high level according to the bit level of the pattern for the entire period.

**NOTE:** The pulse format selection is only available in one of the pattern modes.

## Variable Crossover

For each channel, the cross over of the NRZ signal in PRBS or data mode can be adjusted. This is used to artificially close the eye pattern simulating distortion. Figure 1 shows the normal and complement output with cross over point set to 50% and 70% respectively.

**NOTE:** Variable Crossover feature is available in NRZ mode only.

## Pattern

There are two types of patterns available:

### Data

Arbitrary data up to the maximum available memory per channel can be setup as pattern data.

### PRBS

Predefined PRBS of  $2^5-1$  to  $2^{31}-1$  can be setup as pattern data.

## Data Polarity

In pattern mode the polarity of the data can be set to either normal or inverted. When set to inverted, a logical '1' will become a logical '0' at the output and vice versa.

## Levels

### Pre-defined Levels

Pre-defined levels allow the easy setup of the channels for commonly used logic families. These are: ECL, ECLGND, LVT, LVPCL and LVDS.

### Custom Levels

Levels can be set to custom values in either of two ways:

- low level - high level
- amplitude - offset

### Level Protection

Output levels can be limited to a user defined range to protect the device under test. Level protection can be switched on and off.

### Level Polarity

Level polarity can be set to either normal or inverted. Set to inverted, the low level and the high level values are interchanged.

### Outputs Enable/Disable

Outputs can be switched on and off independently for each channel and for each normal/complement connector.

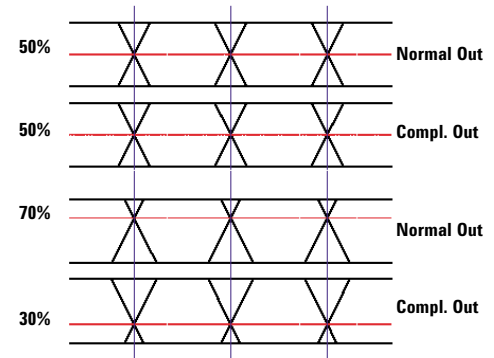


Figure 1

## Auxiliary Channels

### Outputs

#### Trigger Output

The trigger output can be enabled/disabled. The levels of the trigger output can be set as high level - low level pair.

The trigger output can be set to one of the following modes:

- Trigger on clock  
The frequency of the trigger output is set to the system frequency. It can be further divided by n (n= 1, 2, 3, 4, 5, 6, 7... $2^{31}-1$ ).
- Trigger on data  
One Trigger pulse occurs on every start of the repetitive data pattern.

### Inputs

Note: The built in input termination eliminates the need for external bias networks and prevents a degrading of the input sensitivity

#### Clock Input

The clock input can be 'AC' or 'DC' terminated. The 'DC' termination voltage can be set. See also 'Clock Source - external'.

#### Start Input

The start input can be used to start the instruments. When armed the instrument waits for the selected edge of the applied signal.

Parameters:

- Threshold (voltage)
- Edge (rising/falling)
- Termination voltage

## Store/Recall

Allows permanent storage of instrument settings. 9 store/recall registers are provided. Included in the stored settings are all parameters and data.

## Specifications

Specifications describe the instrument's warranted performance. Non-warranted values are described as typical. All specifications apply after a 30 min warm-up with 50 Ohm source/load resistance. All specifications are valid from 0°C to 55°C ambient temperature if not stated otherwise.

### Internal Clock Generation

Period Range	298.5 ps - 66.6 ns
Period Resolution	6 digits, 0.001 ps best case
Frequency Range	15 MHz - 3.35 GHz
Frequency Resolution	1 Hz
Accuracy	50 ppm

### Jitter

Random Jitter (Clock Mode)	<4 ps RMS (1.5ps typical)
Total Jitter (Data Mode)	<5 ps RMS (2ps typical) (<30 ps pp (12ps typical))

### Transition Times

The transition times can be modified by the 'Pulse Performance' selector:

Mode	Description	Typical Transition Times
Fast	Provides faster rise and fall times	60 ps to 70 ps
Normal	Standard setting with guaranteed specifications	70 ps to 80 ps
Smooth	Provides slower rise and fall times and smoother edges	80 ps to 120 ps

### Human Interface

The graphical user interface enables the user to operate the instrument as simply as possible. All parameters are displayed on a color coordinated display. The instrument setup is intuitive. All important parameters are in view at a glance and can be easily accessed and modified with numeric keys, cursors keys or a rotary knob. A content sensitive online help enables users to set up their test configurations quickly and easily.

### Channel Output Timing

Number Of Channels	1, (81133A), 2 (81134A)
Transition Times (10 % To 90 %)	< 90 ps
Transition Times (20 % To 80 %)	< 60 ps
Delay Variation	-5 ns to 230 ns
Delay Resolution	1 ps
Delay Accuracy	± 20 ps
Phase	-6000° to +279000°
Phase Resolution	0.01°, or 1 ps
Skew Between Differential Outputs	< 20 ps nominal
Maximum Skew Range	± 10 ns,
Width Range	100 ps to (period - 100 ps)
Width Resolution	1 ps
Width Accuracy	± 40 ps
Duty Cycle Range	0.15% - 99.85%
Duty Cycle Resolution	0.002 %, or 1 ps
Divide By	1, 2, 4, 8, 16, 32, 64, 128

**Note:** Timing specifications are valid after auto calibration with a maximum temperature variation of ±10°C.

### Channel Output Levels

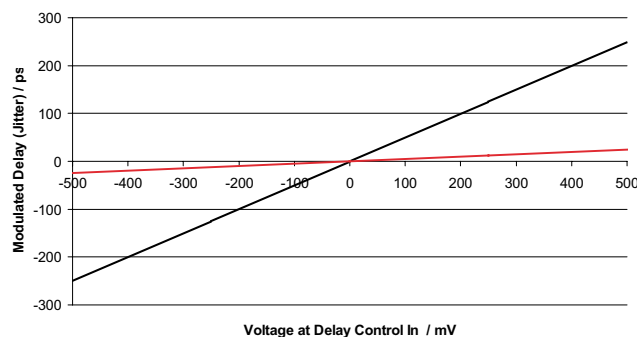
Amplitude	50 mV to 2.00 V
Level Window	-2.00 V to +3.00 V
Level Resolution	10 mV
Level Accuracy	2 % of setting ±20 mV
Amplitude Accuracy	2 % ± 20 mV
Settling Time	1 ns
Overshoot, Ringing	<10 % ± 10 mV differential outputs
Impedance	50 Ohm nominal
Variable Crossover	30% to 70% typical
Maximum External Termination Voltage	-2.00 V to +3.00 V
Short Circuit Current	-80 mA ≤ I <sub>sc</sub> ≤ 120 mA nominal
Limit	High and low levels into 50 Ohm can be limited.
Normal/Complement	Selectable
Disable	Yes (relay)

Data Generation	
Memory Depth	8Kbit per channel/12Mbit extended memory
Data Format	RZ / NRZ / R1

PRBS	$2^n - 1, n = 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 23, 31$	
PRBS	Polynomial	Comment
2 <sup>5</sup> -1	$X^5 + X^4 + X^2 + X^1 + 1$	
2 <sup>6</sup> -1	$X^6 + X^5 + X^3 + X^2 + 1$	ITU-T V.29
2 <sup>7</sup> -1	$X^7 + X^6 + 1$	
2 <sup>8</sup> -1	$X^8 + X^7 + X^3 + X^2 + 1$	
2 <sup>9</sup> -1	$X^9 + X^5 + 1$	CCITT 0.153 / ITU-T V.52
2 <sup>10</sup> -1	$X^{10} + X^7 + 1$	CCITT 0.152 / ITU-T 0.152
2 <sup>11</sup> -1	$X^{11} + X^{10} + 1$	
2 <sup>12</sup> -1	$X^{12} + X^9 + X^8 + X^5 + 1$	
2 <sup>13</sup> -1	$X^{13} + X^{12} + X^{10} + X^9 + 1$	
2 <sup>14</sup> -1	$X^{14} + X^{13} + X^{10} + X^9 + 1$	
2 <sup>15</sup> -1	$X^{15} + X^{14} + 1$	CCITT 0.151 / ITU-T 0.151
2 <sup>23</sup> -1	$X^{23} + X^{18} + 1$	CCITT 0.151 / ITU-T 0.151
2 <sup>31</sup> -1	$X^{31} + X^{28} + 1$	

Trigger Output	
Amplitude	50 mV to 2.00 V
Level Window	-2.00 V . . +3.00 V
Resolution	10 mV
Format Fixed Duty Cycle,	50% nominal
Maximum External Voltage	- 2.00 V . . +3.00 V
Transition Times (20% to 80% of amplitude)	< 100 ps (< 60 ps typical)
Minimum Output Frequency	15 MHz / Divider Factor
Mode Clock Clock divided by 1,2,3, . .	2 <sup>31</sup> -1 or trigger on bit 0 of data
Disable	Yes (relay)

Delay Control Input	
Interface	dc-coupled
Impedance	50 Ohm nominal
Input Levels For Full Modulation Range	±500 mV
Max Input Levels	±2.5 V
Delay Modulation Range	±250 ps, ±25 ps, selectable
Modulation Frequency	0 Hz - 200 MHz



**Figure 2: Modulated Delay (Jitter) vs Voltage Level at Delay-Control-Input for ±250ps and ±25ps settings**

## Pulse Pattern and Data Functionality

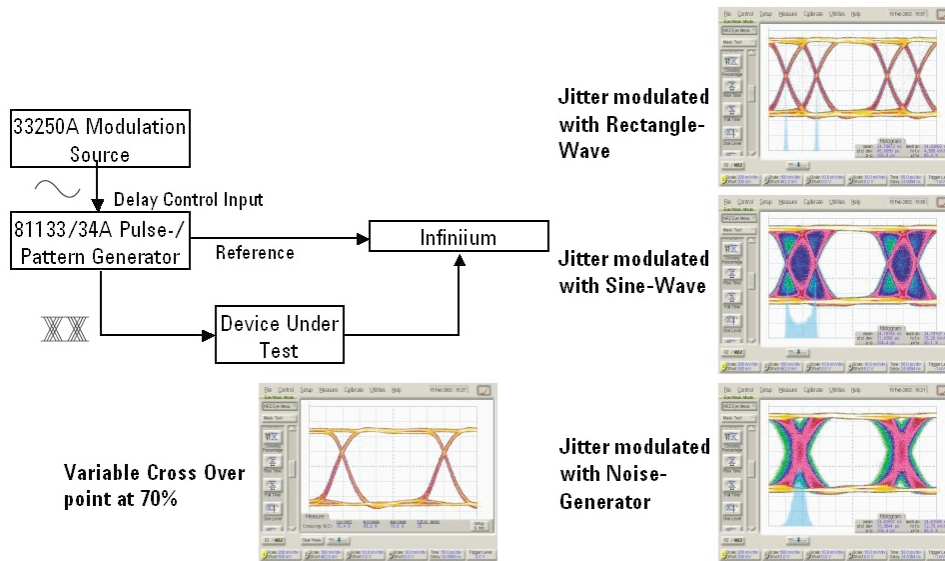
The 81133A and 81134A can generate an 8 KBit digital pattern in NRZ, RZ and R1 mode. Furthermore, the 81133A and 81134A can provide a hardware generated pseudo random binary sequence (PRBS) from 2<sup>5</sup> - 1 to 2<sup>31</sup> - 1.

### Jitter Emulation (Delay Control Input)

Full control over the signal quality of pulse and data signals provides the Delay Control Input. With an external modulation source (e.g. Agilent 33250A) the amount and shape of signal jitter can be varied for stress tests or to emulate real world signals. The external source for jitter modulation is applied to this input. Jitter modulation can be turned on and off individually for each channel. Either one of two fixed sensitivities can be selected ± 25ps or ± 250ps resulting in a total of 50ps or 500ps. The amplitude of the modulated jitter is set by the voltage level of the signal applied to the Delay Control Input.

The Variable Crossover Point feature provides additional control over the signal quality.





**Figure 3**

<b>Clock Input</b>	
Interface	ac-coupled with optional dc termination
Impedance	50 Ohm
Termination Voltage	-2.0 V . . +3.0 V
Minimum Swing	300 mV, tr < 3 ns, 50% duty cycle, sine: 0 dBm
Maximum Amplitude	3 Vpp , ± 5 Vdc
Frequency Measurement	Yes
Period Range	299 ps . . 66.6 ns
Period Resolution	6 digits, 0.001 ps best case
Frequency Range	15 MHz . . 3.35 GHz
Measurement Resolution	100 kHz
Measurement Accuracy	50 ppm

<b>Start Input</b>	
Modes	Start <sup>1</sup>
Interface	dc-coupled
Impedance	50 Ohm nominal
Termination Voltage	-2.0 V . . 3.0 V
Transitions	< 1 ns
Threshold	- 1.8 V to +4 V
Max. Level Window	- 2 V to +5 V

<sup>1</sup> No fixed latency between assertion of start signal and start of output signal

<b>Propagation Delay</b>	
Clock Input to trigger output	8.4 ns nominal, fixed
Trigger Output to channel output	32 ns nominal variable

<b>General Information</b>	
Operating Temperature	0°C to +55°C
Storage Temperature	-40°C to +70°C
Humidity	95% R.H. (0°C to +40°C)
Warm Up	30 Minutes
EMC	Class A
Power	100V to 240V AC nom.; 200 VA max.; 47 Hz to 63 Hz
Net Weight	14.8 kg (32.6 lbs)
Shipping Weight	19 kg (41.9 lbs)
Dimensions	145 mm x 426 mm x 553 mm (5.7 in x 16.75 in x 21.75 in)
Recalibration Period	3 years recommended

<b>Additional Features</b>	
Remote Interfaces	SCPI over GPIB, LAN and USB
Store/Recall Registers (Non Volatile Memory)	9 complete settings can be saved. The last settings are saved when power is turned off.

## Ordering Information

Agilent 81133A 3.35 GHz 1-channel Pulse/ Pattern Generator  
Agilent 81134A 3.35 GHz 2-channel Pulse/ Pattern Generator

### Options

Agilent 8113xA-UK6 Commercial Calibration Certificate with Test Data  
Agilent 8113xA-1CP Rackmount and Handle Kit  
Agilent 1494-0059 Rack Slide Kit  
Agilent N4871A Cable Kit: SMA matched pair,  $t_t=50\text{ps}$  (Recommended for high performance and differential applications)

### Accessories

Agilent 15435A Transition Time Converter 150ps  
Agilent 15432B Transition Time Converter 250ps  
Agilent 15433B Transition Time Converter 500ps  
Agilent 15434B Transition Time Converter 1000ps  
Agilent 15438A Transition Time Converter 2000ps

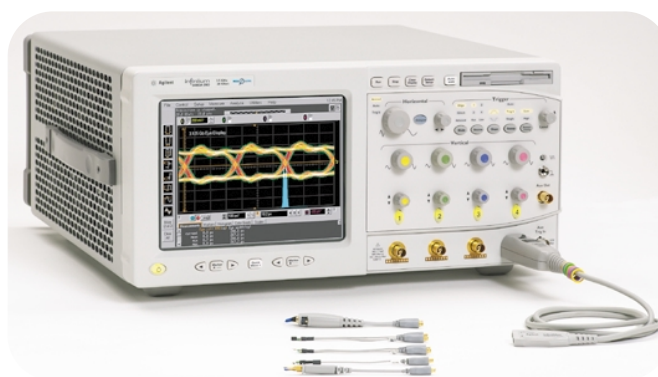
### Warranty and Service

3 years Return-to-Agilent (standard with every order)  
5 years Return-to-Agilent

Commercial Calibration for 5 years  
Standard Compliant Calibration for 5 years

## Measurement Partner

The 6 GHz Infiniium 54855A real time oscilloscope combined with the InfiniiMax 1134A 7 GHz differential and single-ended active probes deliver the highest performance end-to-end measurement system available. With the 20 GSa/s sample rate on each channel, the 54855A is ideal for making signal integrity and jitter measurements for PCI Express, Serial ATA, Gigabit Ethernet, Fibre Channel and other high speed serial bus standards.



## Related Agilent Literature

- *Agilent Family of Pulse/Pattern Generators, Brochure*
- *Agilent 81100 Family Pulse/Pattern, Product Overview*
- *Agilent 3.3GHz Pulse-/Pattern Generators, Photocard*
- *Generating/Measuring Jitter with the Agilent 81134A Pulse/Pattern Generator & 54855A Infiniium Scope, Product Note*
- *Agilent 81133A/81134A 12 Mbit Extended Pattern Memory, Product Note*

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5980-1215E  
5988-5935EN  
5988-9411EN  
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(fax) (65) 6836 0252  
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